

FIG. 1

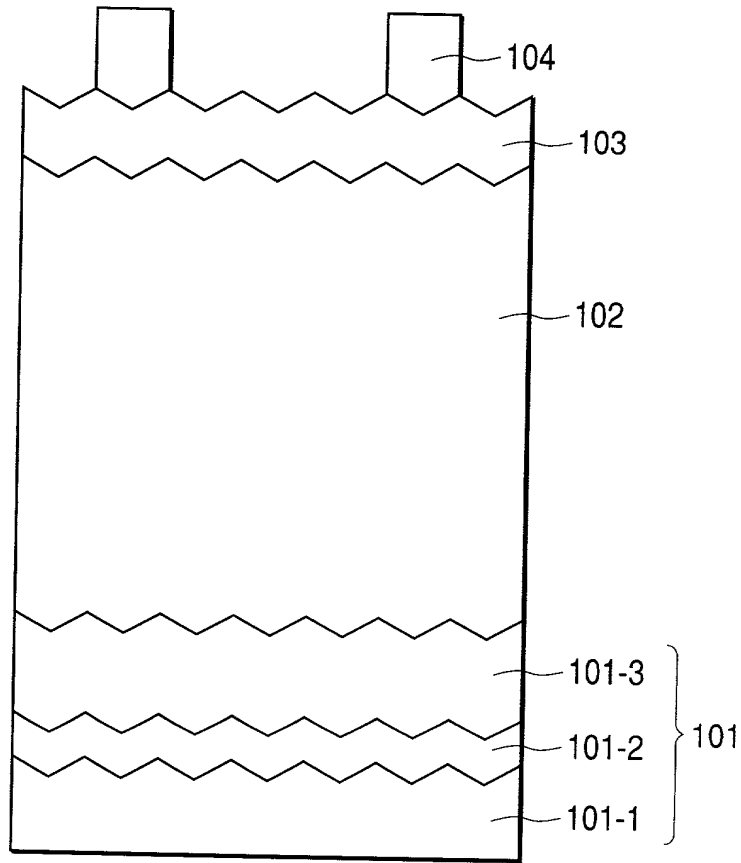


FIG. 2

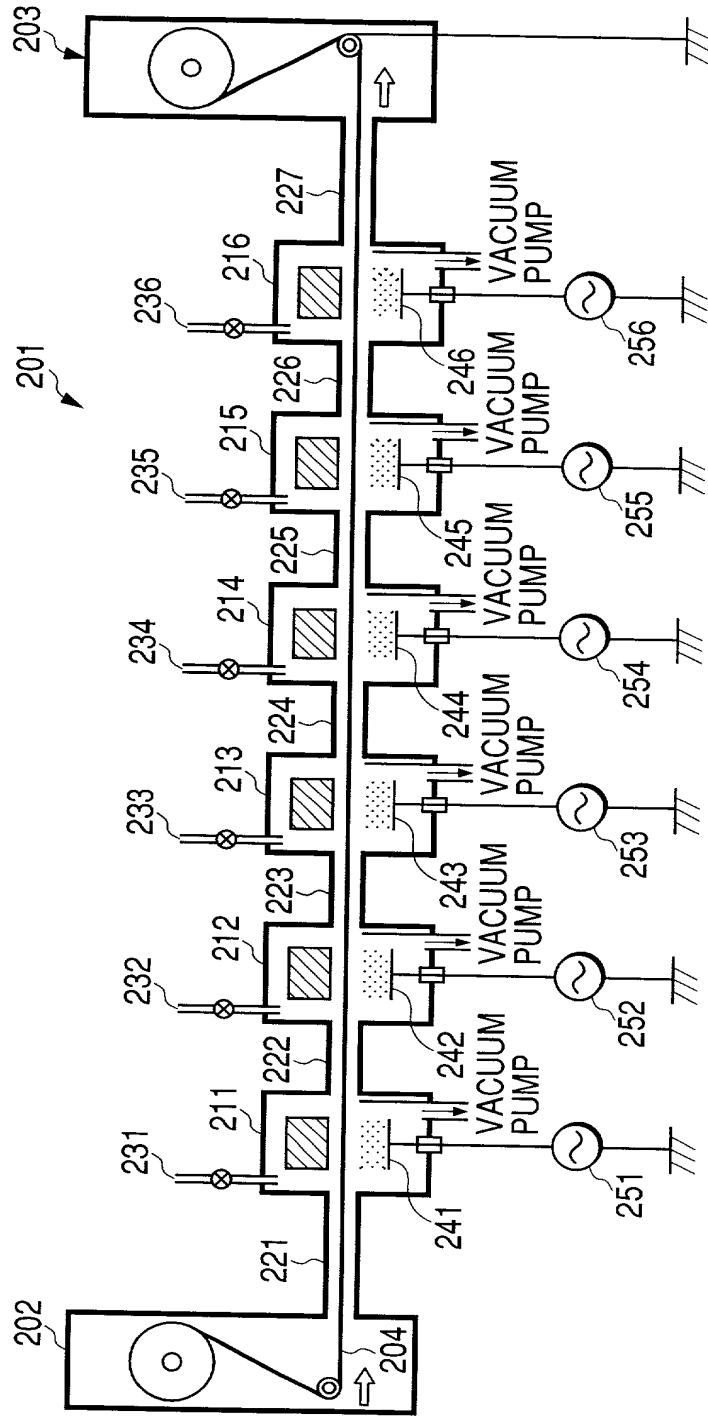


FIG. 3

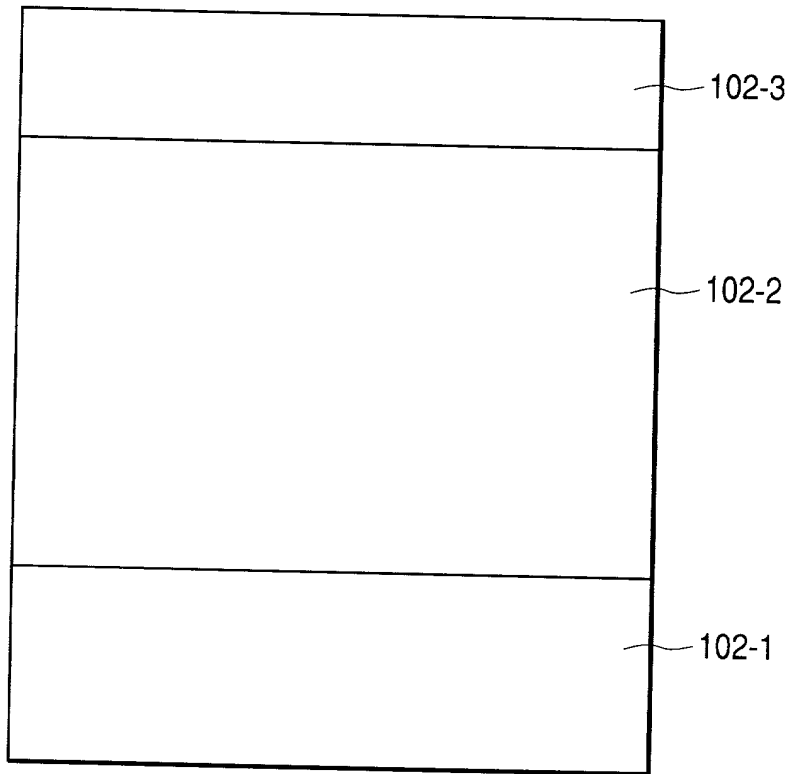
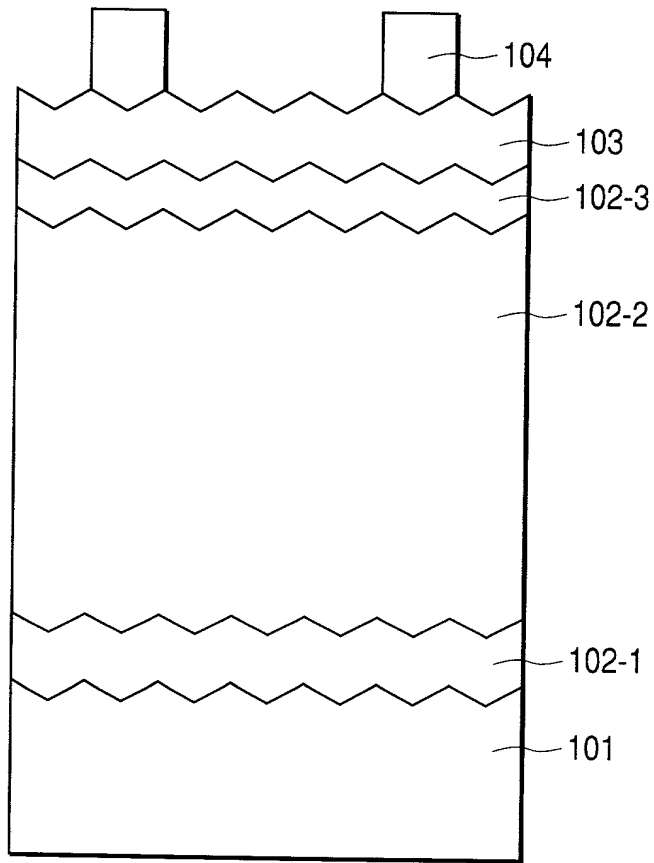


FIG. 3

FIG. 4



Copyright © 2004 Intel Corporation. All rights reserved.

FIG. 5

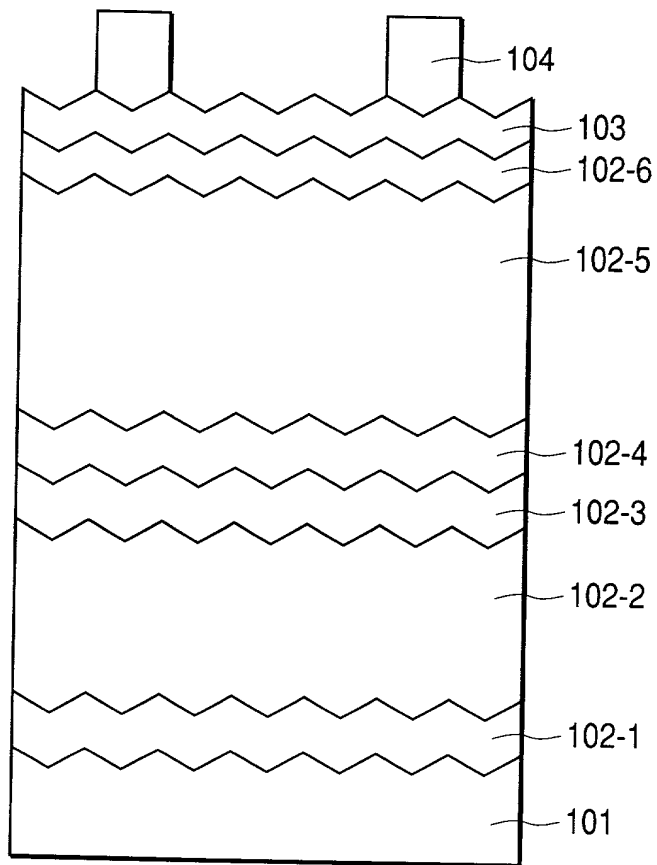


FIG. 5 is a cross-sectional view of a layered structure. The structure includes a substrate 101, a first layer 102-1, a second layer 102-2, a third layer 102-3, a fourth layer 102-4, a fifth layer 102-5, a sixth layer 102-6, a seventh layer 103, and two protrusions 104.